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7. The scanning velocity modulation deflection signal generator of claim 1, wherein said variable conduction device is a transistor.
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REMARKS

The specification has been amended to include a reference to the priority application.

The above amendments to the claims have been made to eliminate reference indicia and to meet the requirements of the USPTO.

No fee is believed to have been incurred by virtue of this amendment. However, if a fee is incurred on the basis of this amendment, please charge such fee against deposit account 07-0832.

Respectfully submitted,

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MARKED UP CLAIMS

1. (AMENDED) A scanning velocity modulation deflection signal generator, comprising:

a variable conduction device [(Q1)] having a first input [(Q1e)] responsive to a scanning velocity modulation deflection signal [(Vm)], and a second input [(Q1b)] responsive to a control signal;

in a first condition said device [(Q1)] providing a feedback path [(Q1e-Q1c)] for controlling said [a] scanning velocity modulation deflection signal [(Vm)] in magnitude; and,

in a second condition said device [(Q1)] interrupting said feedback path [(Q1e-Q1c)] and inhibiting generation of said scanning velocity modulation deflection signal [(Vm)].

2. (AMENDED) The scanning velocity modulation deflection signal generator of claim 1, wherein during said first condition said variable conduction device [(Q1)] varies conduction in accordance with said magnitude of said scanning velocity modulating deflection signal [(Vm)].

3. (AMENDED) The scanning velocity modulation deflection signal generator of claim 2, wherein said variable conduction device [(Q1)] varies conduction to variably attenuate a scanning velocity modulating signal [(SVM)] in accordance with said scanning velocity modulating deflection signal [(Vm)] magnitude.

4. (AMENDED) The scanning velocity modulation deflection signal generator of claim 1, wherein during said second condition said variable conduction device [(Q1)] is fully conductive responsive to said control signal for inhibiting said scanning velocity modulation deflection signal [(Vm)].

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5. (AMENDED) The scanning velocity modulation deflection signal generator of claim 1, wherein during said second condition said variable conduction device [(Q1)] is fully conductive, attenuating a scanning velocity modulating signal [(SVM)] and inhibiting generation of said scanning velocity modulation deflection
5 signal [(Vm)].

6. (AMENDED) The scanning velocity modulation deflection signal generator of claim 1, wherein said second condition conduction in said variable conduction device [(Q1)] is unresponsive to [of] said scanning velocity modulating deflection
10 signal [(Vm)].

7. (AMENDED) The scanning velocity modulation deflection signal generator of claim 1, wherein said variable conduction device [(Q1)] is a transistor.

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